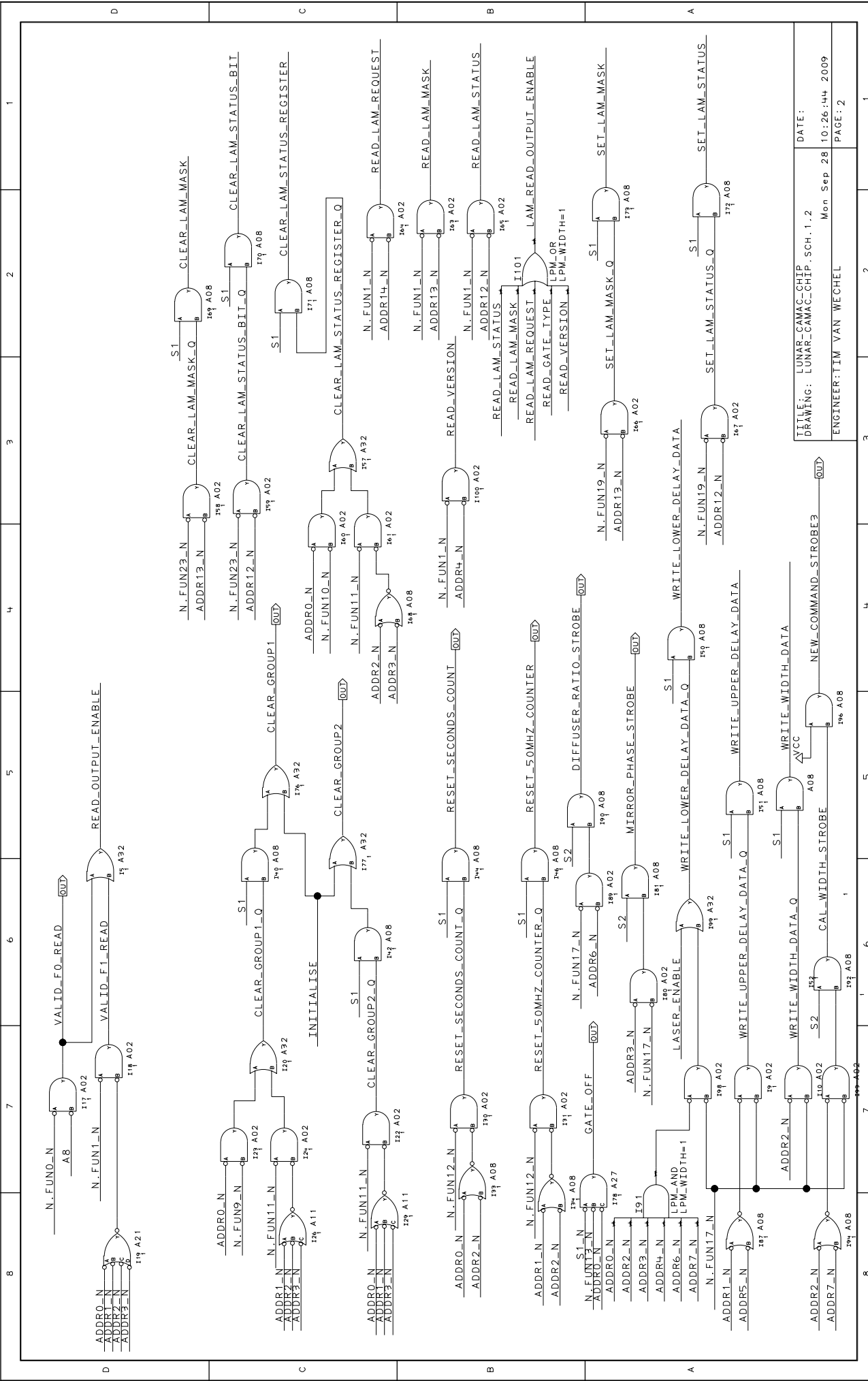
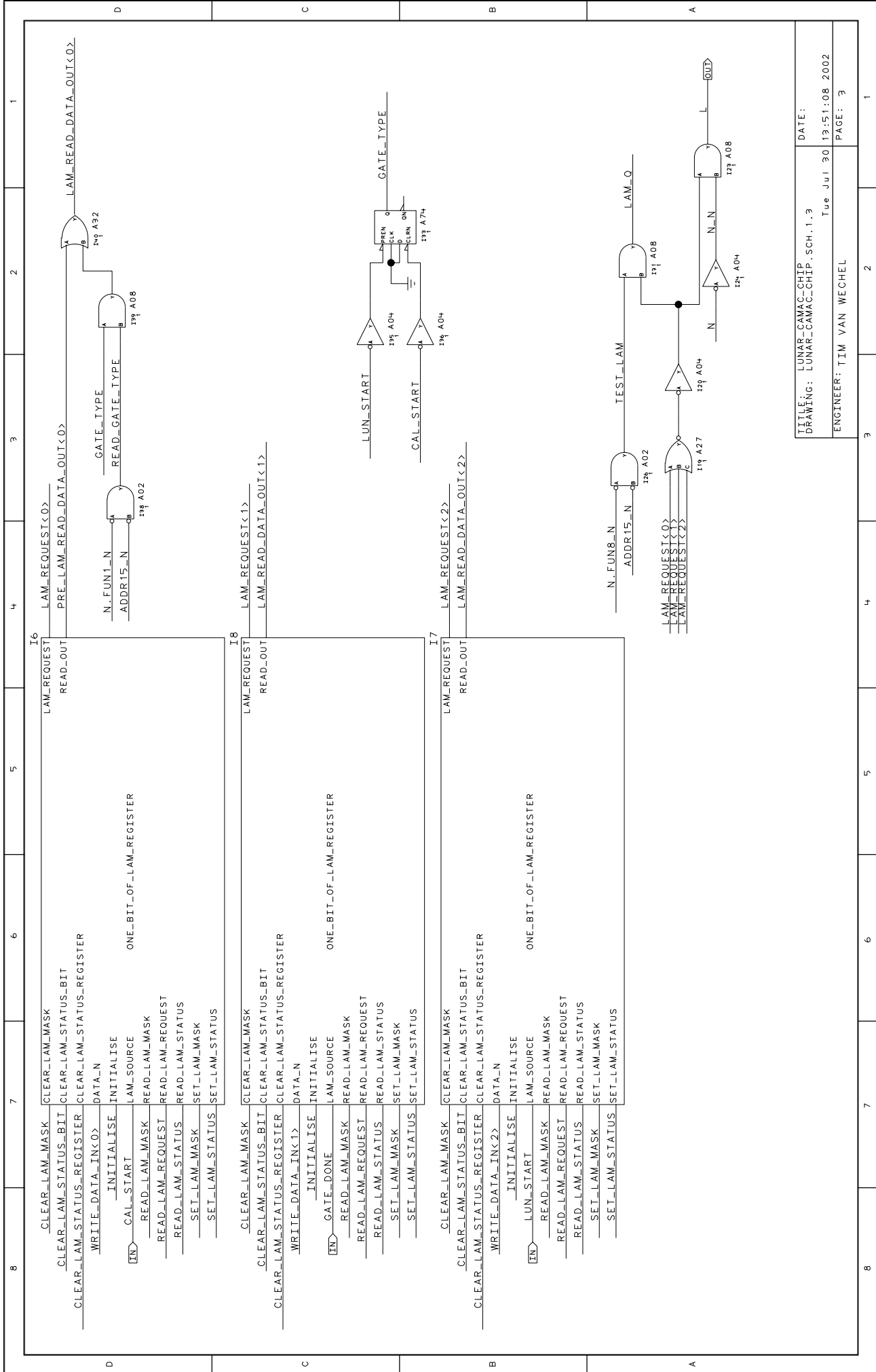


TITLE: LUNAR-CAMAC-CHIP
DRAWING: LUNAR-CAMAC-CHIP.SCH.1.1
DATE: Thu Oct 1 08:11:55 2009

ENGINEER:TIM VAN WECHEL
PAGE: 1



TITLE: LUNAR-CAMAC-CHIP
 DRAWING: LUNAR-CAMAC-CHIP_SCH_1.1.2
 DATE: Mon Sep 28 10:26:44 2009
 ENGINEER: TIM VAN WECHEL



```

I6
LAW_REQUEST
READ_OUT
CLEAR_LAM_MASK
CLEAR_LAM_STATUS_BIT
CLEAR_LAM_STATUS_BIT
CLEAR_LAM_STATUS_REGISTER
WRITE_DATA_IN<0>
DATA_N
INITIALISE
INITIALISE
LUN_START
READ_LAM_MASK
READ_LAM_MASK
LAM_SOURCE
READ_LAM_REQUEST
READ_LAM_REQUEST
READ_LAM_STATUS
READ_LAM_STATUS
SET_LAM_MASK
SET_LAM_MASK
SET_LAM_STATUS
SET_LAM_STATUS
ONE_BIT_OF_LAM_REGISTER

```

```

I8
LAW_REQUEST
READ_OUT
CLEAR_LAM_MASK
CLEAR_LAM_STATUS_BIT
CLEAR_LAM_STATUS_BIT
CLEAR_LAM_STATUS_REGISTER
WRITE_DATA_IN<1>
DATA_N
INITIALISE
INITIALISE
GATE_DONE
GATE_DONE
LUN_START
READ_LAM_MASK
READ_LAM_MASK
LAM_SOURCE
READ_LAM_REQUEST
READ_LAM_REQUEST
READ_LAM_STATUS
READ_LAM_STATUS
SET_LAM_MASK
SET_LAM_MASK
SET_LAM_STATUS
SET_LAM_STATUS
ONE_BIT_OF_LAM_REGISTER

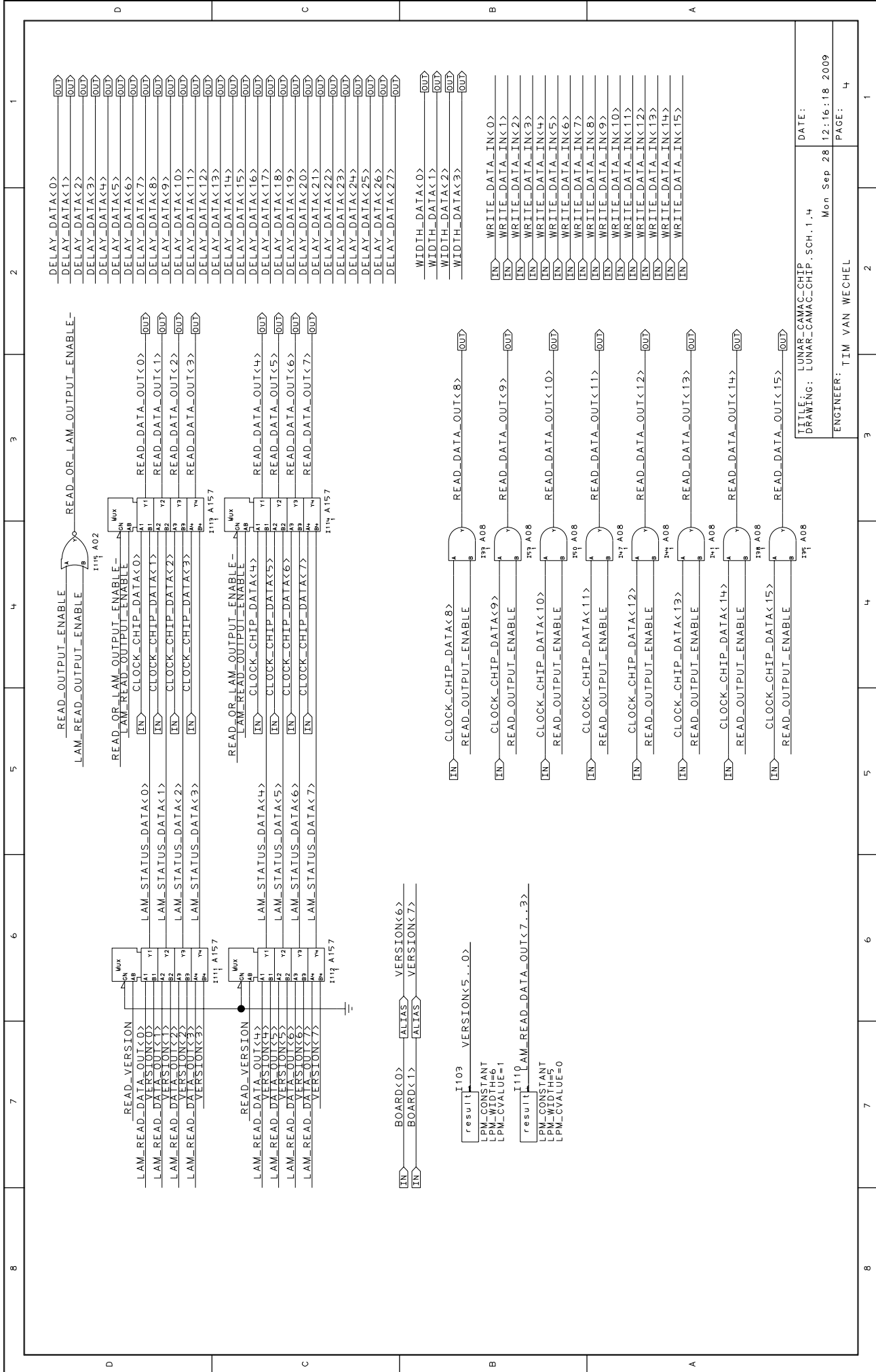
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I7
LAW_REQUEST
READ_OUT
CLEAR_LAM_MASK
CLEAR_LAM_STATUS_BIT
CLEAR_LAM_STATUS_BIT
CLEAR_LAM_STATUS_REGISTER
WRITE_DATA_IN<2>
DATA_N
INITIALISE
INITIALISE
LUN_START
LUN_START
READ_LAM_MASK
READ_LAM_MASK
LAM_SOURCE
READ_LAM_REQUEST
READ_LAM_REQUEST
READ_LAM_STATUS
READ_LAM_STATUS
SET_LAM_MASK
SET_LAM_MASK
SET_LAM_STATUS
SET_LAM_STATUS
ONE_BIT_OF_LAM_REGISTER

```

TITLE: LUNAR_CAMAC_CHIP
 DRAWING: LUNAR_CAMAC_CHIP.SCH.1.3
 DATE: Tue Jul 30 13:51:08 2002
 ENGINEER: TIM VAN WECHEL
 PAGE: 3



8 7 6 5 4 3 2 1

D C B A

READ_VERSION
LAM_READ_DATA_OUT<0>
LAM_READ_DATA_OUT<1>
LAM_READ_DATA_OUT<2>
LAM_READ_DATA_OUT<3>
LAM_READ_DATA_OUT<4>
LAM_READ_DATA_OUT<5>
LAM_READ_DATA_OUT<6>
LAM_READ_DATA_OUT<7>
LAM_READ_DATA_OUT<8>
LAM_READ_DATA_OUT<9>
LAM_READ_DATA_OUT<10>
LAM_READ_DATA_OUT<11>
LAM_READ_DATA_OUT<12>
LAM_READ_DATA_OUT<13>
LAM_READ_DATA_OUT<14>
LAM_READ_DATA_OUT<15>
LAM_READ_DATA_OUT<16>
LAM_READ_DATA_OUT<17>
LAM_READ_DATA_OUT<18>
LAM_READ_DATA_OUT<19>
LAM_READ_DATA_OUT<20>
LAM_READ_DATA_OUT<21>
LAM_READ_DATA_OUT<22>
LAM_READ_DATA_OUT<23>
LAM_READ_DATA_OUT<24>
LAM_READ_DATA_OUT<25>
LAM_READ_DATA_OUT<26>
LAM_READ_DATA_OUT<27>

READ_VERSION
LAM_READ_DATA_OUT<4>
LAM_READ_DATA_OUT<5>
LAM_READ_DATA_OUT<6>
LAM_READ_DATA_OUT<7>

BOARD<0>
BOARD<1>
ALIAS
ALIAS
VERSION<6>
VERSION<7>

result
T103
VERSION<5...0>
LPM_CONSTANT
LPM_WIDTH=6
LPM_CVALUE=1

result
T110
LAM_READ_DATA_OUT<7...3>
LPM_CONSTANT
LPM_WIDTH=5
LPM_CVALUE=0

READ_OR_LAM_OUTPUT_ENABLE=
LAM_READ_OUTPUT_ENABLE
READ_OR_LAM_OUTPUT_ENABLE=
LAM_READ_OUTPUT_ENABLE

CLOCK_CHIP_DATA<0>
CLOCK_CHIP_DATA<1>
CLOCK_CHIP_DATA<2>
CLOCK_CHIP_DATA<3>

READ_OR_LAM_OUTPUT_ENABLE=
LAM_READ_OUTPUT_ENABLE
CLOCK_CHIP_DATA<4>
CLOCK_CHIP_DATA<5>
CLOCK_CHIP_DATA<6>
CLOCK_CHIP_DATA<7>

READ_OR_LAM_OUTPUT_ENABLE=
LAM_READ_OUTPUT_ENABLE
CLOCK_CHIP_DATA<8>
CLOCK_CHIP_DATA<9>
CLOCK_CHIP_DATA<10>
CLOCK_CHIP_DATA<11>
CLOCK_CHIP_DATA<12>
CLOCK_CHIP_DATA<13>
CLOCK_CHIP_DATA<14>
CLOCK_CHIP_DATA<15>

READ_OR_LAM_OUTPUT_ENABLE=
LAM_READ_OUTPUT_ENABLE
CLOCK_CHIP_DATA<16>
CLOCK_CHIP_DATA<17>
CLOCK_CHIP_DATA<18>
CLOCK_CHIP_DATA<19>
CLOCK_CHIP_DATA<20>
CLOCK_CHIP_DATA<21>
CLOCK_CHIP_DATA<22>
CLOCK_CHIP_DATA<23>
CLOCK_CHIP_DATA<24>
CLOCK_CHIP_DATA<25>
CLOCK_CHIP_DATA<26>
CLOCK_CHIP_DATA<27>

WIDTH_DATA<0>
WIDTH_DATA<1>
WIDTH_DATA<2>
WIDTH_DATA<3>

WRITE_DATA_IN<0>
WRITE_DATA_IN<1>
WRITE_DATA_IN<2>
WRITE_DATA_IN<3>
WRITE_DATA_IN<4>
WRITE_DATA_IN<5>
WRITE_DATA_IN<6>
WRITE_DATA_IN<7>
WRITE_DATA_IN<8>
WRITE_DATA_IN<9>
WRITE_DATA_IN<10>
WRITE_DATA_IN<11>
WRITE_DATA_IN<12>
WRITE_DATA_IN<13>
WRITE_DATA_IN<14>
WRITE_DATA_IN<15>

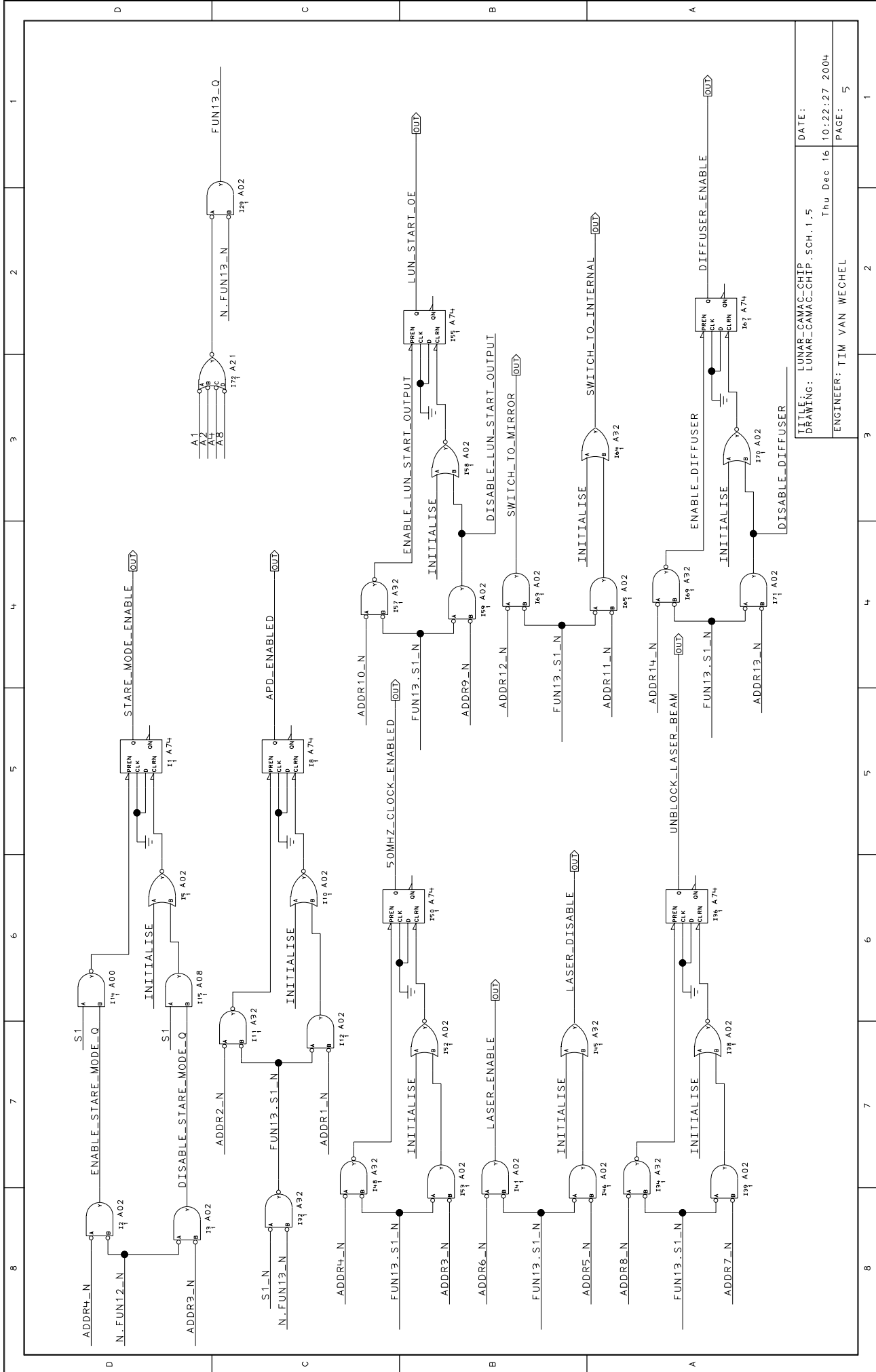
READ_DATA_OUT<8>
READ_DATA_OUT<9>
READ_DATA_OUT<10>
READ_DATA_OUT<11>
READ_DATA_OUT<12>
READ_DATA_OUT<13>
READ_DATA_OUT<14>
READ_DATA_OUT<15>

DATE:
LUNAR_CAMAC_CHIP
LUNAR_CAMAC_CHIP.SCH.1.14
Mon Sep 28 12:16:18 2009

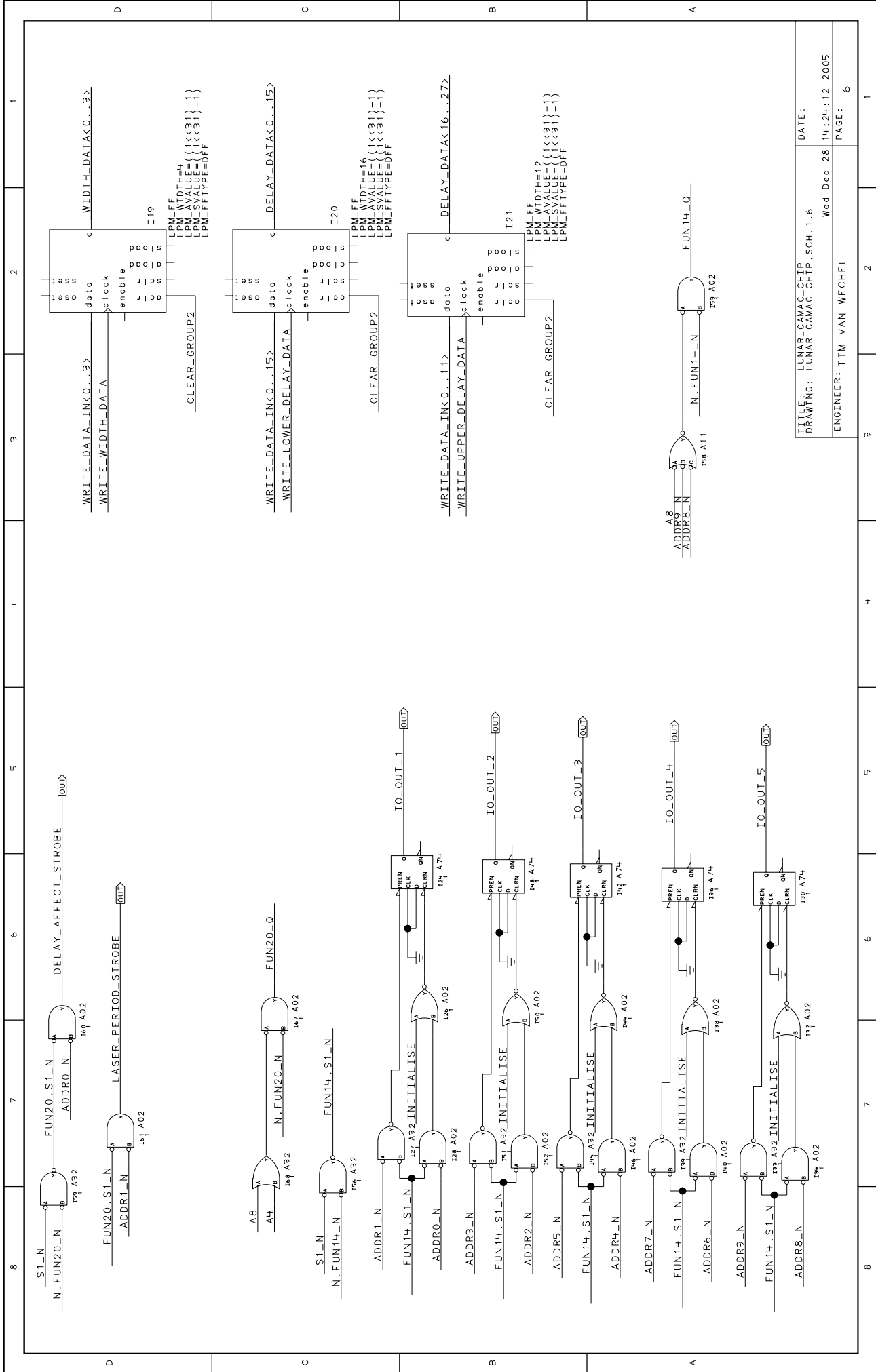
ENGINEER: TIM VAN WECHEL
PAGE: 4

8 7 6 5 4 3 2 1

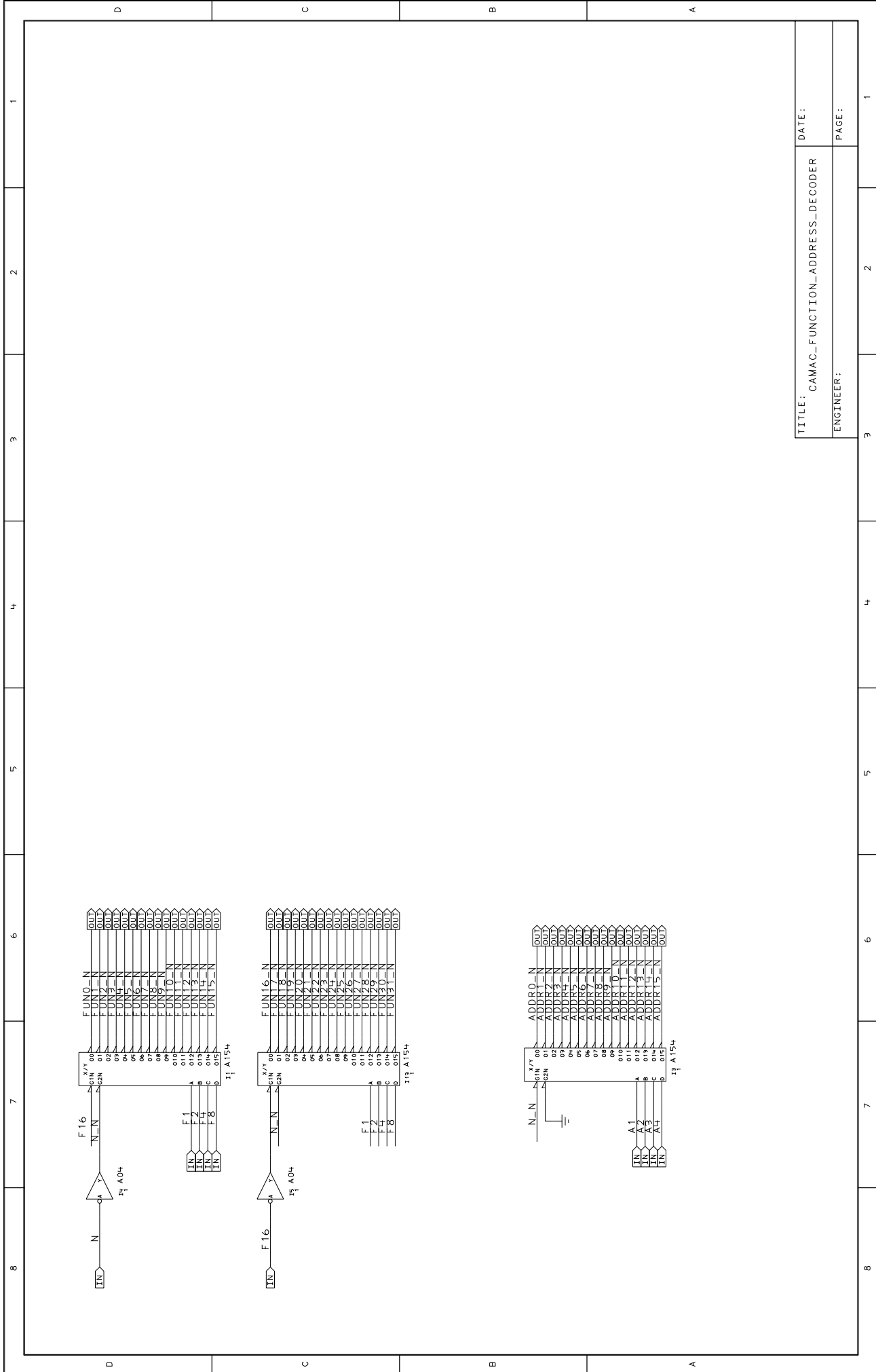
D C B A



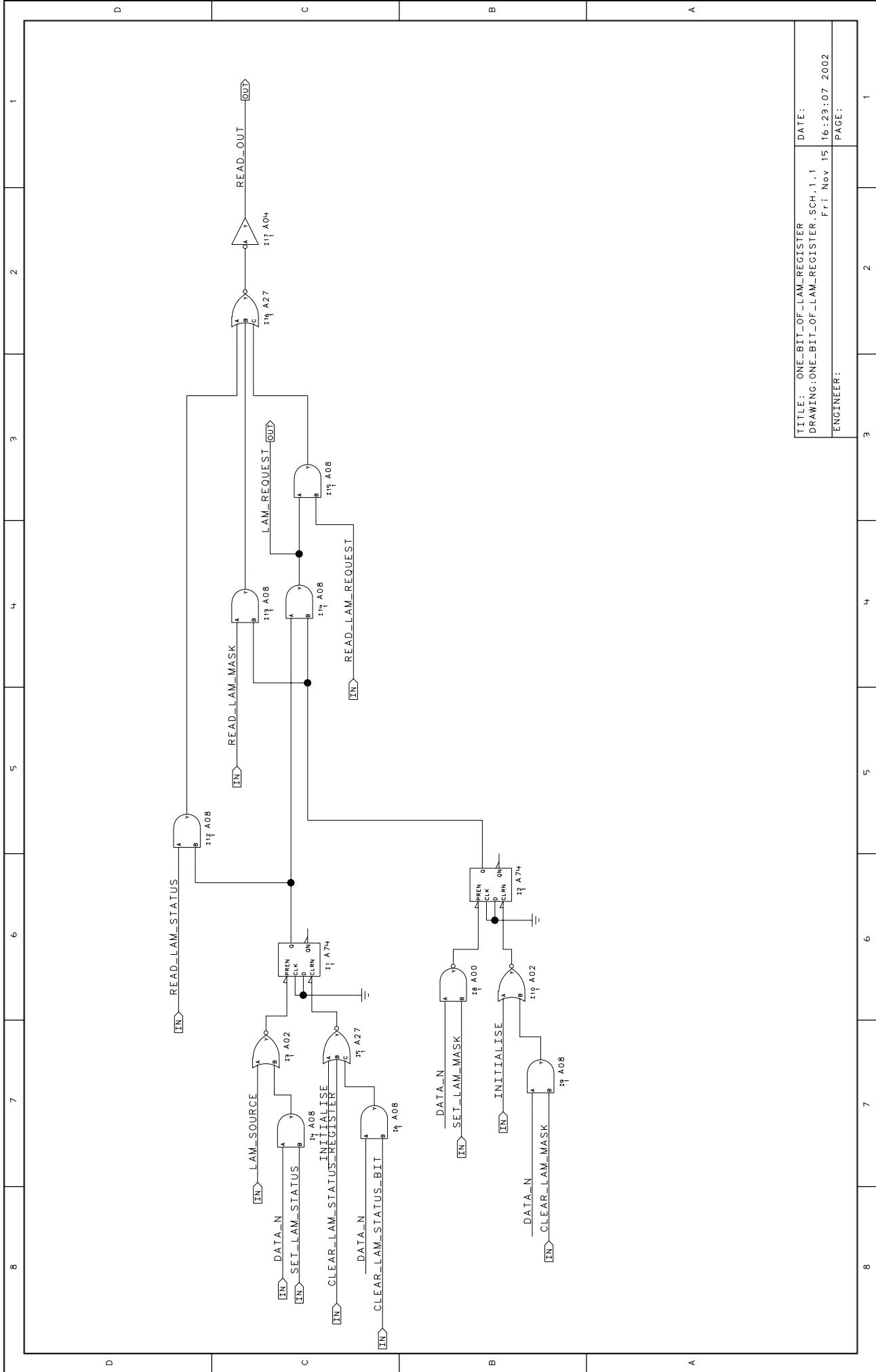
TITLE: LUNAR-CAMAC-CHIP
 DRAWING: LUNAR-CAMAC-CHIP.SCH.1.5
 DATE: Thu Dec 16 10:22:27 2004
 ENGINEER: TIM VAN WECHEL
 PAGE: 5



TITLE: LUNAR_CAMAC_CHIP
 DRAWING: LUNAR_CAMAC_CHIP.SCH.1.6
 DATE: Wed Dec 28 14:24:12 2005
 ENGINEER: TIM VAN WECHEL
 PAGE: 6



TITLE: CAMAC_FUNCTION_ADDRESS_DECODER
 ENGINEER:
 DATE:
 PAGE:



TITLE: ONE_BIT_OF_LAM_REGISTER
 DRAWING: ONE_BIT_OF_LAM_REGISTER_SCH_1_1
 ENGINEER: Fri Nov 15 16:23:07 2002
 DATE:
 PAGE: